

**ADVERTISEMENT FOR WALK IN INTERVIEW FOR JRF**

Applications are invited from Indian national candidates possessing the below mentioned qualifications and experience. Candidates should bring along with them application with completed bio-data as per the prescribed format, giving full details of educational qualifications and experience with attested copies of mark sheets / certificates and experience certificates of the candidate addressed to the Director, Punjab Engineering College (Deemed to be University) with recent passport size photographs. Following positions are available for a period of **Three Years** to work in a Science and Engineering Research Board (SERB), Department of Science & Technology (DST), GOI sponsored research project “van der Waals heterostructure based self-switching diode” with Dr. Arun Kumar Singh, Associate Professor, Department of Electronics and Communication Engineering, Punjab Engineering College (Deemed to be University), Chandigarh.

<b>Job Title</b>	: Junior Research Fellow (JRF)
<b>No of Posts</b>	: 01
<b>Fellowship</b>	: As per DST Norms
<b>Essential Qualification</b>	: (i) M.E./M.Tech. (Electronics and Communication Engineering)/ M.E./M.Tech. (Nanotechnology) or equivalent with valid NET / GATE score. <b>OR</b> (ii) M.Sc. (Nanotechnology)/M.Sc. (Electronics)/M.Sc. (Physics) preferably with valid NET/ GATE score.
<b>Desirable</b>	: <ul style="list-style-type: none"><li>• Familiarity and hands-on experience on electronic device fabrication and characterisation tools including EBL, CVD, Mask Aligner, RIE, SEM, AFM and SMUs.</li><li>• Relevant experience in simulation and theoretical modelling of micro-/nano-electronic devices using softwares like Silvaco, Comsol, Synopsys, Matlab etc.</li></ul>
<b>Age Limit</b>	: 28 years on the last date of application. Age relaxation for SC/ST/OBC and PWD candidates is applicable as per Government of India norms
<b>Job Specifications/ Responsibilities</b>	: To perform work assigned and assist the PI in various phases of the research project.
<b>Tenure of Post</b>	: Initial appointment will be for one year, which is extendable up to 3 years solely based on performance. <b><u>The position is coterminous with the project.</u></b>
<b>Date</b>	: 24.06.2019
<b>Time</b>	: 10:00 AM
<b>Venue of Interview</b>	: Seminar Room (Electronics and Communication Engg. Department), Punjab Engineering College (Deemed to be University), Sector-12, Chandigarh

**Note:**

1. The applicant will be responsible for the authenticity of information, other documents and photographs submitted.
2. The Institute reserves the right to accept application at any time, and consider candidates of exceptional credentials. Qualification and experience may be relaxed by the Institute at any point of time for otherwise exceptional candidates.
3. Mere, possessing the prescribed qualification does not ensure that the candidate would be selected. The Candidates will be selected on the basis of merit, performance in interview and need of the project.
4. Candidates have to present themselves for the interview on the interview date with filled in application form, updated CV and original and attested photocopies of mark sheets/ certificates in support of their academic qualifications.
5. Applicants in employment (private, government or any other organization) are required to submit a “No Objection Certificate” from the employer at the time of interview.
6. Candidates for whom the result of qualifying degree is awaited, would have to compulsorily produce the result of their qualifying degree latest by July 31, 2019, failing which their appointment would automatically stand cancelled.
7. No TA/DA will be paid for appearing in the interview.

**For any clarification contact:**

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