One Day National Seminar
Semiconductor Technology for Space and Defense Applications

OBJECTIVE OF THE SEMINAR
The technological advancements in semiconductor technology for developing smaller, faster and cheaper integrated circuits (ICs) has driven the industry to nanometer scale node and in a new era of high-performance three-dimensional transistor structures. The speed, computational power, and enhanced functionality of ICs based on this advanced technology promises to transform both our work and leisure environments. On the other hand, Silicon scaling has continued to provide the foundation for new product innovations in Imaging, Materials, and Process to counter scaling challenges using cost-effective ways while maintaining manufacturability and reliability for defense and space applications. The implementation of nanotechnology has introduced variety of manufacturing issues as well as set the stage for a range of manufacturing challenges that requires revolutionary new process methodologies beyond Silicon. The main objective of this seminar is to provide a forum for scientists, faculty and students to update their technical thoughts by interacting with technologist carrying out leading edge research and development in the area of Semiconductor devices. The seminar addresses all of the manufacturing issues and challenges with technical depth and conceptual clarity, and presents solutions to novel set of problems of future electronics. The seminar will provide an insight to both theoretical and practical knowledge for the design, analysis, and operation of semiconductor devices in order to meet the needs of relevant industry, space, defense and research organizations.

TOPICS TO BE COVERED
• Semiconductor device fabrication and characterization techniques
• Innovation, & Novel Approaches to “Scaling”
• CMOS, alternative device structures and new materials
• Future of logic, SOI, memory devices and process integration, as well as 3D packaging.

LIST OF RESOURCE PERSONS.
• Scientists/Engineers from DRDO/ ISRO /CSIR labs etc.
• Faculty from IISc, IITs and Institute of national repute.

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REGISTRATION FORM
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Type of Participation: (Please Tick)
Students / JRFs / SRFs, ..........................
Teachers ,Scientists, Engineer from Industries and Research organizations ..........................
Venue
Senate Hall , Administrative Building (Near helicopter), PEC Chandigarh
Time
09:00 AM Onwards
Registration Details
Advance registration is recommended through E-mail
(Last date to register: October 11, 2019)
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Chandigarh Subsection
Chandigarh Chapter