

TIME SCHEDULE (E-TENDER) FOR RE-TENDER FOR CIRCUIT BOARD PLOTTER FOR PCB PROTOTYPES AT ELECTRONICS AND COMMUNICATION ENGINEERING DEPT.

I	Date of publication	23.08.2018
I	Downloading of e-tender document	Start date: 23.08.2018 10:00 AM End date: 13.09.2018 05:00 PM
III	Date of submission of e-tender	Start date: 23.08.2018 10:00 AM End date: 13.09.2018 05:00 PM
IV	Physical submission of EMD and other necessary pre-qualification documents (scanned copies)	Start date: 23.08.2018 10:00 AM End date: 13.09.2018 05:00 PM
V	Opening of Technical Bid (online)	14.09.2018 02:00 PM
VI	Opening of Financial Bid of only eligible technically qualified bidder as determined by the Committee.	To be informed after checking eligibility of Bidders
VII	Place of opening of bids	Seminar Room Dept. of Electronics and Comm. Engg. Punjab Engineering College (Deemed to be University), Sector 12, Chandigarh 160012
VIII	Address for communication	Head, Dept. of Electronics and Comm. Engg. Punjab Engineering College (Deemed to be University), Sector 12, Chandigarh 160012