

PG – Curriculum Structure

Course/ Credits Distribution of ME Electronics(VLSI Design)

Sr. No.	Courses	Credit Structure	
		No. of Courses	Credits
1	Program Core	04	12 (3 each)
2	Open core	02	06 (3 each)
3	Program Elective	03	09 (3 each)
4	Open Elective	01	03
5	Program Lab	02	04 (2 each)
6	Case Histories and industry Experiences	01	01
7	Seminar and Technical Writing	01	01
8	Project/ Industry based Project	02	12+18=30
Total Credits			66

Program Core

Course Code	Course Name	L T P	Credits
EVN511	Program Core-I : Electronics System Design	3-0-0	3
EVN513	Program Core-II : Digital VLSI Design	3-0-0	3
EVN520	Program Core-III : Computer Aided VLSI Design	3-0-0	3
EVN521	Program Core IV: Testing And Fault Tolerance	3-0-0	3
Total			12

Open core

Course Code	Course Name	L T P	Credits
	Program Mathematics/ Mathematics (Open Core I):	3-0-0	3
EVN544	Open core-II : Design of Experiments and Research Methodology	3-0-0	3
Total			06

Program Elective-I (Any One)

Course Code	Course Name	L T P	Credits
EVN514	Low Power Design Techniques	3-0-0	3
EVN515	Real Time Systems	3-0-0	3
EVN516	Design Of Semiconductor Memories	3-0-0	3
Total			03

Program Elective-II (Any One)

Course Code	Course Name	L T P	Credits
EVN512	Microelectronics	3-0-0	3
EVN517	Analog CMOS Design	3-0-0	3
EVN524	FPGA Based System Design	3-0-0	3
Total			03

Program Elective-III (Any One)

Course Code	Course Name	L T P	Credits
EVN522	Embedded Systems	3-0-0	3
EVN523	Advanced Digital Signal Processing	3-0-0	3
EVN527	Advances In VLSI Design	3-0-0	3
Total			03
Program Elective Total		9-0-0	09

Open Elective

Course Code	Course Name	L T P	Credits
EVN525	Advanced Virtual Instrumentation	3-0-0	3
EVN529	Neural Networks	3-0-0	3
Total			03

Program Lab

Course Code	Course Name	L T P	Credits
EVN518	Program Lab-I :	0-0-3	2
EVN528	Program Lab-II :	0-0-3	2
Total			04

Case Histories and industry Experiences

Course Code	Course Name	L T P	Credits
EVN548	Case Histories and industry Experiences	0-0-2	1
Total			01

Seminar and Technical Writing

Course Code	Course Name	L T P	Credits
EVN549	Seminar and Technical Writing	0-0-2	1
Total			01

Project/ Industry based Project

Course Code	Course Name	L T P	Credits
EVN598	Project/ Industry based Project -I	0-0-24	12
EVN599	Project/ Industry-based Project -II	0-0-36	18
Total			30

CONSOLIDATED SCHEME-ME Electronics(VLSI Design)

Sem							Lecture Course	L	T	P	Weekly Contact	Credits
I	Program Core -I Electronics System Design EVN511 (LTP: 3 0 0)	Program Core-II Digital VLSI Design EVN513 (LTP: 3 0 0)	Program Mathematics/Mathematics (Open core I) (LTP: 3 0 0)	Program Elective I (see list of Electives) (LTP: 3 0 0)	Program Elective II (see list of Electives) (LTP: 3 0 0)	Program Lab I EVN518 (LTP: 0 0 3)	5	15	0	3	18	17
II	(Open Core II) Design of Experiments & Research Methodology : EVN544 (LTP: 3 0 0)	Program Core-III Computer Aided VLSI Design EVN520 (LTP: 3 0 0)	Program Core-IV Testing and Fault Tolerance EVN521 (LTP: 3 0 0)	Program Elective III (see list of Electives) (LTP: 3 0 0)	Open Elective (See list of open electives) (LTP: 3 0 0)	Program Lab II EVN528 (LTP: 0 0 3)	5	15	0	3	18	17
III	Case History and Industry Experiences : EVN548 (LTP: 0 0 2)	Seminar & Technical Writing : EVN549 (LTP: 0 0 2)	Project/ Industry Based Project -I :EVN598 (LTP: 0 0 24)	-	-	-	-	0	0	28	28	14
IV	Project/ Industry Based Project-II: EVN599 (LTP: 0 0 36)	-	-	-	-	-	-	0	0	0	36	18

PG (Electronics VLSI Design) – Curriculum Structure

Semester I

Sr. No.	Course Code	Course Name	L	T	P	Credits
1	EVN511	Program Core-I : Electronics System Design	3	0	0	3
2	EVN513	Program Core-II : Digital VLSI Design	3	0	0	3
3		Program Mathematics/ Mathematics (Open Core I):	3	0	0	3
4		Program Elective-I	3	0	0	3
5		Program Elective-II	3	0	0	3
6	EVN518	Program Lab-I :	0	0	3	2
Total						17

Semester II

Sr. No.	Course Code	Course Name	L	T	P	Credits
1	EVN544	Open core-II : Design of Experiments and Research Methodology	3	0	0	3
2	EVN520	Program Core-III : Computer Aided VLSI Design	3	0	0	3
3	EVN521	Program Core IV: Testing And Fault Tolerance	3	0	0	3
4		Program Elective-III	3	0	0	3
5		Open Elective*	3	0	0	3
6	EVN528	Program Lab-II:	0	0	3	2
Total						17

*It could be intra/inter departmental ME course

Semester III

Sr. No.	Course Code	Course Name	L	T	P	Credits
1	EVN548	Case Histories and industry Experiences	0	0	2	1
2	EVN549	Seminar and Technical Writing	0	0	2	1
3	EVN598	Project/ Industry based Project -I	0	0	24	12
Total						14

Semester IV

Sr. No.	Course Code	Course Name	L	T	P	Credits
1	EVN599	Project/ Industry-based Project -II	0	0	36	18
Total						18

List of Electives

Program Elective-I (Any One)

Course Code	Course Name	L T P	Credits
EVN514	Low Power Design Techniques	3-0-0	3
EVN515	Real Time Systems	3-0-0	3
EVN516	Design Of Semiconductor Memories	3-0-0	3
Total			03

Program Elective-II (Any One)

Course Code	Course Name	L T P	Credits
EVN512	Microelectronics	3-0-0	3
EVN517	Analog CMOS Design	3-0-0	3

EVN524	FPGA Based System Design	3-0-0	3
Total			03

Program Elective-III (Any One)

Course Code	Course Name	L T P	Credits
EVN522	Embedded Systems	3-0-0	3
EVN523	Advanced Digital Signal Processing	3-0-0	3
EVN527	Advances In VLSI Design	3-0-0	3
Total			03
Program Elective Total		9-0-0	09

Open Elective

Course Code	Course Name	L T P	Credits
EVN525	Advanced Virtual Instrumentation	3-0-0	3
EVN529	Neural Networks	3-0-0	3
Total			03