

Department of Electronics and Communication Engineering Punjab Engineering College (Deemed to be University), Chandigarh

Advertisement for the One (01) post of Junior Research Fellow (JRF) in C2S (Meity) PROJECT

Applications are invited from the suitable candidates for the **One (01) post of Junior Research Fellow (JRF)** to work on the Research project funded by Ministry of Electronics & Information Technology (MeitY), Govt. of India under Chips to Startup (C2S) Programme at Punjab Engineering College (Deemed to be University), Sector-12, Chandigarh. The detailed information is given below:

Description		Details
Job Title	:	Junior Research Fellow (JRF)
No of Posts	:	One (01 Nos.)
Project Title	:	Memristor- FPGA hybrid hardware system for brain inspired analog computing
Sponsoring Agency	:	Ministry of Electronics & Information Technology (MeitY), Govt. of India under Chips to
		Startup (C2S) Programme
Fellowship	:	Rs 37,000/-per month plus HRA as per DST/Institute norms
Essential	:	B.Tech./M.Sc./M.E./M.Tech./MS (R) in Electronics / Electronics & Communication/
Qualification		Electronics and Electrical/ Electronics and Instrumentation / Computer Science and
		Engineering/ Mircoelectronics/ VLSI Design/ Microelectronics and VLSI Design
		/Nanoelectronics/ Embedded System / Digital Systems, and any other related areas of
		Electronics Engg.
Age Limit	:	28 years on the last date of application. Age relaxation for SC/ST/OBC and PWD candidates
		is applicable as per Government of India norms
Job Specifications/	:	Design on EDA tools, fabrication and characterisation of memristors etc., and to perform
Responsibilities		various works assigned & to assist the Chief Investigator (CI) in various phases of the
		research project.
Tenure of Post/	:	Initial appointment will be for one year, which is extendable till the project duration solely
Nature of		based on the performance. The appointment will be on purely temporary basis and the
Appointment		position is coterminous with the project.
Last Date to Apply	:	22.07.2025 (Tuesday) (Application form can be downloaded from www.pec.ac.in)
How to Apply	:	Candidates should submit their application through E-mail/Speed Post/by hand to the
		address given below on before the last date.

Only shortlisted candidates will be communicated through E-mail to appear in the interview to be held in hybrid mode (online/offline) on 25.07.2025 (Friday) and no other communication in this regard will be entertained.

Note:

- 1. The applicant will be responsible for the authenticity of information, other documents and photographs submitted.
- 2. The Institute reserves the right to accept application at any time, and consider candidates of exceptional credentials. Qualification and experience may be relaxed by the Institute at any point of time for otherwise exceptional candidates.
- 3. Mere possessing the prescribed qualification does not ensure that the candidate would be selected. The Candidate will be selected on the basis of merit, performance in interview and need of the project.
- 4. Shortlisted Candidates have to present themselves for the interview on the interview date with updated CV and original and attested photocopies of mark sheets/ certificates in support of their academic qualifications.
- 5. Applicants in employment (private, government or any other organization) are required to submit a "No Objection Certificate" from the employer at the time of interview.
- 6. No TA/DA will be paid for appearing in the interview.
- 7. Selected candidates will be encouraged to register for the PhD Programme at PEC Chandigarh as per the Institute norms/ Rules.

Address for Submission of Application:

Dr. Arun Kumar Singh, Chief Investigator

Professor

Department of Electronics and Communication Engineering

Punjab Engineering College (Deemed to be University)

Sector-12, Chandigarh-160012 (India) Mobile: +91-9815912699;9417429699

E-mail: arun@pec.edu.in

(Application form can be downloaded from www.pec.ac.in)