

## Department of Electronics and Communication Engineering Punjab Engineering College (Deemed to be University), Chandigarh

## Advertisement for the post of Junior Research Fellow (JRF) in SERB-DST (CRG) PROJECT

Applications are invited from the suitable candidates for the **one (01) post of Junior Research Fellow (JRF)** to work on the Core Research Grant (CRG) project sponsored by SERB-DST at Punjab Engineering College (Deemed to be University), Sector-12, Chandigarh. The detailed information is given below:

Description		Details
Job Title	:	Junior Research Fellow (JRF)
No of Posts		01
Project Title	:	Novel GaN Self-Switching Diode Based Frequency Multipliers
Sponsoring Agency	:	Science and Engineering Research Board (SERB), Department of Science & Technology (DST), Govt. of India under Core research Grant (CRG)
Fellowship		Rs 31,000/-per month plus HRA as per DST norms
Essential Qualification	:	<ul> <li>(i) M.E./M.Tech. (Electronics and Communication Engineering/ VLSI/ Embedded/Microelectronics/Nanotechnology/Microwave Engg.) or equivalent preferably with NET / GATE score.</li> <li>OR</li> </ul>
		(ii) M.Sc. (Electronics)/M.Sc. (Physics)/ M.Sc. (Nanotechnology) or equivalent preferably with NET/GATE score.
Age Limit	:	28 years on the last date of application. Age relaxation for SC/ST/OBC and PWD candidates is applicable as per Government of India norms
Job Specifications/ Responsibilities	:	To perform work assigned and assist the PI in various phases of the research project.
Tenure of Post/	:	Initial appointment will be for one year, which is extendable up to maximum 3 years solely
Nature of		based on the performance. The appointment will be on purely temporary basis and the
Appointment		position is coterminous with the project.
Last Date to Apply	:	20.01.2023 (Friday) (Application form can be downloaded from www.pec.ac.in)
How to Apply	:	Candidates should submit their application through <b>E-mail/Speed Post/by hand</b> to the address given below on before the last date.

Only shortlisted candidates will be communicated through E-mail to appear in the interview and no other communication in this regard will be entertained.

Note:

- 1. The applicant will be responsible for the authenticity of information, other documents and photographs submitted.
- 2. The Institute reserves the right to accept application at any time, and consider candidates of exceptional credentials. Qualification and experience may be relaxed by the Institute at any point of time for otherwise exceptional candidates.
- 3. Mere possessing the prescribed qualification does not ensure that the candidate would be selected. The Candidate will be selected on the basis of merit, performance in interview and need of the project.
- 4. Shortlisted Candidates have to present themselves for the interview on the interview date with updated CV and original and attested photocopies of mark sheets/ certificates in support of their academic qualifications.
- 5. Applicants in employment (private, government or any other organization) are required to submit a "No Objection Certificate" from the employer at the time of interview.
- 6. <u>No TA/DA will be paid for appearing in the interview</u>.
- 7. Selected Candidate may also register for PhD program at PEC as per the Institute norms/ Rules

## Address for Submission of Application:

Dr. Arun Kumar Singh, PI Associate Professor Department of Electronics and Communication Engineering Punjab Engineering College (Deemed to be University) Sector-12, Chandigarh-160012 (India) Mobile: +91-9417429699; 9815912699 E-mail: arun@pec.edu.in; arunkumar.singh@outlook.com (Application form can be downloaded from www.pec.ac.in)