

NOTIFICATION

PhD admission Notice (Full Time Only) for 2026-27

(Under Visvesvaraya PhD scheme for Electronics and IT: Phase-II)

PEC proudly announces that the Institute has been selected as one of the Centre for prestigious Visvesvaraya PhD Scheme for Electronics and IT: Phase-II supported by Ministry of Electronics & Information Technology (Meity), Govt. of India. **One (01) fellowship is available under C2S Project of MeitY at Dept. of Electronics & Communication Engineering.** Post Graduate students in relevant fields of Engineering/ Technology who are interested in pursuing research in relevant areas may apply for admission to a full time PhD program with fellowship under Visvesvaraya PhD scheme for Electronics and IT: Phase-II for 2026-27.

Applications are invited from eligible candidates for admission to PhD programme on Full Time basis for Academic Session 2026-27 in the Department of Electronics & Communication Engineering (**01 no.** under C2S Project) under the '**Visvesvaraya PhD Scheme for Electronics and IT: Phase- II**'. A consistently good academic career will be preferred and selection will be made on the basis of overall academics, entrance test for Non-GATE/ Non-UGC NET candidates and interview performance. **Candidates having GATE/ UGC NET (any category) are exempted from the entrance test.** The interested candidates can submit the application form along with relevant enclosures as per guidelines available on website of Institute. The candidates are required to pay non-refundable fee (online) of Rs. 1000/- for General and Other Categories, Rs. 500/- for SC/ST.

<u>Important Dates:</u>	Starting date for submission of applications: 28.09.2026 Closing date for submission of applications is up to: 15.10.2026.
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The major features of the scheme for the full-time scholars are given below:

Fellowship: Rs. 38,750/- per month in 1st and 2nd year and @Rs. 43,750 per month in 3rd, 4th and 5th years of PhD (support till PhD completion or 5 years whichever is earlier)

Reimbursement of Rent (RoR): This component is linked with the fellowship of PhD Candidate. The rate of RoR is 24%, 16% & 8% (of fellowship) for X, Y & Z class cities/towns respectively. The classification of the cities is as per the notification issued by the Ministry of Finance for the reimbursement of HRA.

Research contingency Grant Support: An amount of Rs. 1,20,000/Year for Full-time PhD candidate.

Support for attending International conferences: Support upto Rs. 1.5 lakhs/Full- time PhD candidate.

Visit to Labs abroad: A support for each selected Full-Time PhD candidate upto Rs.10.5 lakhs for visit to labs abroad.

No. of position:

- **One (01)** under C2S Project of MeitY at Electronics and Communication Engineering Department including.

Doctor of Philosophy (PhD) Programme under Visvesvaraya PhD scheme for Electronics and IT in the following thrust areas of research.

Department	Research Area	Minimum education Qualification
Electronics & Communication Engineering	Semiconductor Devices/VLSI	M.Tech/ME in ECE/CSE/IT/Nano Technology in relevant areas or MS Physics/Electronics/Nano Technology or in equivalent relevant areas

Eligibility Criteria Essential:

Master's Degree with Bachelor's in Engineering/Technology/Sciences with First Class or 60% marks (55% marks for SC/ST/PwD)/CPI or CGPA greater than 6.5 (6 for SC /ST/ PwD) on a 10-point scale.

Preference will be given to the candidate having a GATE/UGC NET (any category). For more details visit : <https://pec.ac.in/admissions/phd>

Application Procedure

1. The applications form is to be downloaded from institute website and filled with all relevant documents attached hard copy of the same **application must be sent** to following address by hand/speed post/registered post so as to reach by the prescribed date.

Office of Dean Academic Affairs
Punjab Engineering College
(Deemed to be University)
Sector-12, Chandigarh - 160012

2. All applications which are not in prescribed form / without relevant supporting enclosures /received after the last date/ without application fees will be out rightly rejected. No correspondence shall be entertained in this regard. Interim Correspondence will also not been entertained or replied to.
3. Institute will not be responsible under any circumstances for any sort of postal delay/ loss of the application form.

4. **The last date for submission of application is 15.10.2026 upto 17:00 Hrs.**
5. The Application Fee is to be deposited through online payment through any payment mode in the following account: -

Bank Details

Name of Account Holder	Punjab Engineering College-Admissions
Institute Name with Complete Address	State Bank of India
Branch Name With Complete Address	PEC Branch, Sector 12, Chandigarh
IFSC Code of The Branch	SBIN0002452
MICR Code	160002008
Bank Account Number (Saving Account)	37646936859

6. Candidates are required to write his/her name and contact number on the UPI/NEFT/RTGS Receipt.
7. The Application Fee is non-refundable.
8. The envelope containing the Application Form shall mention on the top in bold **“Application for PhD Programme Under Visvesvaraya PhD Scheme: Phase-II under C2S Project at ECE Dept.”** with the candidate's name, address and phone number written on it.
9. No TA/DA and/or Lodging/Boarding will be paid for attending the Entrance Test / Interview, no accommodation shall be provided.
10. Any corrigendum /changes /updates shall be made available only on the Institute's Website viz. <https://pec.ac.in/>. The candidates are advised to visit the Institute's Website regularly for any information/updates on the PhD Admission Process.

Selection Process for Admission

1. **Candidates having GATE/ UGC NET (any category) are exempted from the entrance test.**
2. For all other candidates an entrance test will be conducted for admission under Visvesvaraya PhD scheme.
3. The PhD Admissions shall be based on offline **Entrance Test and Offline Interview.**
 - a. Entrance test will be a multiple choice test; each question will have four options with only one correct answer.
 - b. The entrance test will carry 90 marks and it will have two parts, Part A of 30 marks and Part B of 60 marks. Qualifying marks in the entrance test will be overall 50%.
 - c. Part A will have 30 questions of 1 mark each on Research Methodology. It will be common to all the students and its duration will be 30 minutes.

- d. Part B will be stream/ discipline specific and it will have 60 questions of 1 mark each. Its duration will be 60 minutes.
- e. Candidates who have secured 50% marks in the entrance test are eligible to be called for interview.
- f. A relaxation of 5% marks will be allowed in the entrance examination for the candidates belonging to SC/ST/differently-abled category.

Tentative Admission Schedule

Sr. No.	Particulars	Tentative Dates
1	Advertisement and availability of application forms	28.09.2026
2	Last date for submission of completed application forms	15.10.2026
3	Display of list of eligible candidates called for Entrance Test for all categories	16.10.2026
4	Entrance Test and display of the list of candidates qualified for interview.	22.10.2026 (Fore Noon)
5	Offline Interview (for all categories)	22.10.2026 (After Noon)
6	Declaration of Results	27.10.2026
7	Payment of fees (Online)	27.10.2026 to 28.10.2026
8	Reporting of PhD students in the respective departments and Semester Begins	29.10.2026

Submission of Documents/Certificates:

The **self-attested copies** of the following documents are required to be attached with the hardcopy Application Form, failing which the application shall not be evaluated and shall stand summarily rejected:

1. The hard copy of the Prescribed Application Form is duly signed on all the pages.
2. 10th Standard Certificate.
3. 12th Standard Certificate/Diploma Certificate.
4. Graduation Mark-Sheets for all years.
5. Graduation Degree Certificate.
6. Master's Degree & Mark-Sheets for all semesters.
7. GATE/ UGC NET score card (if applicable)
8. SC/ST/PwD Certificates (wherever applicable) in prescribed pro-forma of Central Government Only.
9. Scanned Copy of payment receipt.

Original Documents and ID Proof would have to be produced during the documentation verification Process to be undertaken on the date of Interview failing which the Candidate may not be allowed to appear for the Selection Process.

General Terms & Conditions:

1. The decision of the Director, PEC shall be final in all the matters related to Admission Process.
2. The Institute reserves its right to cancel/postpone/hold the Admission Process at any stage without citing any reason thereof.
3. Mere eligibility doesn't ensure selection. The Institute reserves the right to select a limited number of PhD Candidates in each of the Departments at the sole discretion of the Director, PEC.
4. The list of short-listed candidates shall be displayed on the Institute's Website. Candidates are advised to visit the PEC website <https://pec.ac.in/> regularly. No correspondence regarding reasons for not shortlisting the candidate shall be entertained.
5. The candidates are required to have at least one working email and mobile number which will be used during the entire admission process for communication, if required.
6. In case of any inadvertent mistake in the process of admission detected at any stage even after the registration of the candidate for PhD Programme, the Institute reserves the right to modify/withdraw/cancel any communication made to the candidates.
7. Clarification or queries in the context of the Admission Process may be sent on the email id viz. admissions@pec.edu.in.