

**List of candidates Eligible for Interview for
Ph.D. Admission (Under Visvesvaraya PhD scheme for Electronics and IT: Phase-II)
to be held on 24.02.2023 (Afternoon)**

Sr. No.	Name	Father's Name	Department
1	Kanika Sharma	Satish Kumar Sharma	Computer Science and Engineering
2	Sushmita Chauhan	Mohinder Singh Chauhan	Computer Science and Engineering
3	Ashima Sood	Rajesh Sood	Computer Science and Engineering
4	Vikas Sharma	Trilok Chand Sharma	Electronics & Communication Engineering
5	Priyanka	Sarnam Singh	Electronics & Communication Engineering

Note: Candidates are required to report at respective departments on 24.02.2023 at 2.00 pm