



EXPLORE INNOVATE EXCEL
SINCE 1921

DST-STUTI HANDS-ON TRAINING PROGRAM ON SEMICONDUCTOR DEVICE FABRICATION AND CHARACTERISATION

JOINTLY ORGANIZED BY:
SEMICONDUCTOR RESEARCH CENTER, DEPT. OF ELECTRONICS AND COMM. ENGG.,
PUNJAB ENGINEERING COLLEGE (DEEMED TO BE UNIVERSITY), CHANDIGARH
&
INDIAN INSTITUTE OF TECHNOLOGY (IIT), GANDHINAGAR

FUNDED BY:
DEPARTMENT OF SCIENCE AND TECHNOLOGY, GOVERNMENT OF INDIA

OBJECTIVES OF THE PROGRAM

The proposed training program is designed in line with the STUTI program of DST. The training program aims to provide hands-on-training on utilizing the unique properties of semiconductor material for design, fabrication and characterisation of next generation of electronic devices using, state-of-the-art clean room facilities.

21ST- 27TH NOVEMBER, 2022



HIGHLIGHTS OF THE TRAINING

DEVELOP KNOWLEDGE ABOUT VARIOUS NEW RESEARCH AREAS AND FABRICATION TECHNIQUES IN THE FIELD OF SEMICONDUCTOR MATERIALS, SENSORS, ENERGY HARVESTING, ETC

PROVIDE HANDS-ON TRAINING ON STATE-OF-THE-ART SEMICONDUCTOR FABRICATION AND CHARACTERIZATION EQUIPMENTS LIKE PECVD, MASK ALIGNER SYSTEM ETC.



PRACTICAL TRAINING TO UTILIZE SIMULATION TOOLS LIKE SILVACO TCAD & ANSYS HFSS.

SYNTHESIS AND GROWTH OF THIN FILMS USING VARIOUS TECHNIQUES

ABILITY TO OPERATE AND MANAGE VARIOUS CLEAN ROOM FACILITIES.

WHO CAN ATTEND

- B.Tech. / M.Tech. / M.Sc. / Ph.D Students / JRFs / SRFs (In Relevant Areas)
- Faculty / Scientists / Post-Doc Fellows/ Industry Engineers.
- Not more than 3 people from one institute.

HOW TO REGISTER FOR WORKSHOP

- Interested participants are requested to submit all the details on the following link and upload NOC from institute.
- This event is FREE to attend. Seats are limited. Advance registration is recommended.
- Shortlisted / selected participants will be provided accommodation and food etc. on all the days of Training Program.

LAST DATE TO REGISTER :
31ST OCTOBER, 2022

REGISTRATION LINK: <https://forms.gle/VR4QbXxcF16MzvSS7>

COORDINATOR
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