

Schedule for Ph.D Admission (2017-18) in Electrical Engineering

Sr.No.		Date	Time	Venue
1.	Written Test	17-7-2017	11.00am – 12.00pm	L-9
2.	Declaration of Written Test Result	17-7-2017	2.30pm	Deptt. Notice Board
3.	Verification of Documents	18-7-2017	9.30am	L-9
4.	Interview	18-7-2017	11.00am	HOD Office

Head (EED)

CC:

1. Notice Board
2. Webmaster (Please upload the same on the institute website.)
3. O/I Ph.D Admission (2017)