

ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT

Memo No. PEC/ECE/ 484

Dated: 11/07/2017

Controller of Admission (Ph.D)

Subject: Schedule for Ph.D Admissions 2017 to be uploaded on PEC website.

You are requested to kindly upload the following schedule for written test & interview on Institute website for Ph.D. admissions 2017 in ECE Department.

Date of Written test & Interview	: 18 th July 2017
Reporting for all candidates	: 09:00 am (ECE Dept.)
Written Test (for non-gate candidate)	: 09:30 am to 10:30 am (Venue L-22)
Result Declaration (Written Test)	: 12:00 noon
Documents Verification (for Gate Candidates)	: 09:30 am to 11:00 am in Seminar Room (ECE Dept.)
Documents Verification (for Non Gate Candidates)	: 12:00 noon (onwards)
Interview	: 02:00 pm (onwards), (HOD office)

15/7/17.
Prof. & Head
ECE Deptt.

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15/7/17

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